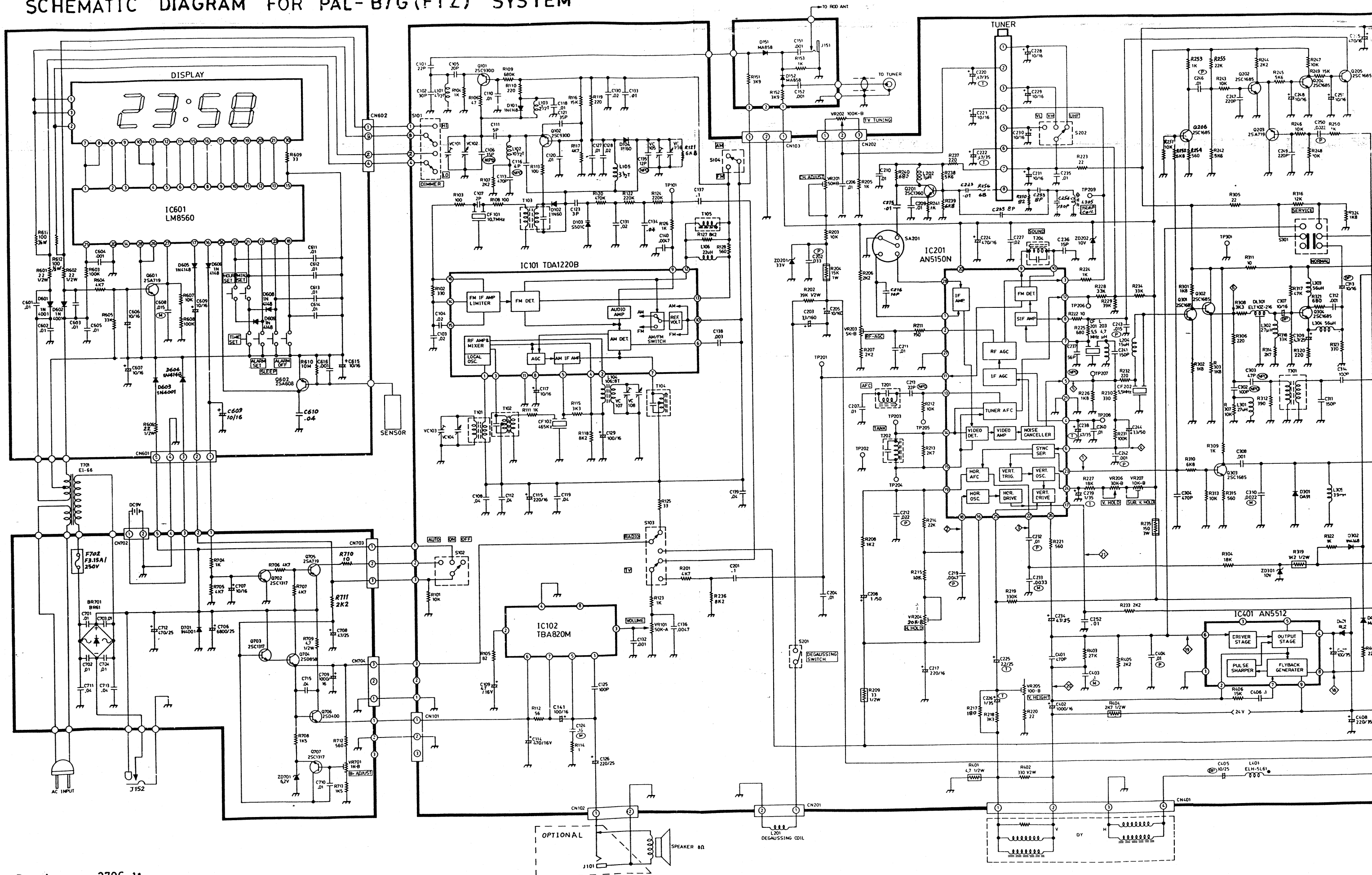
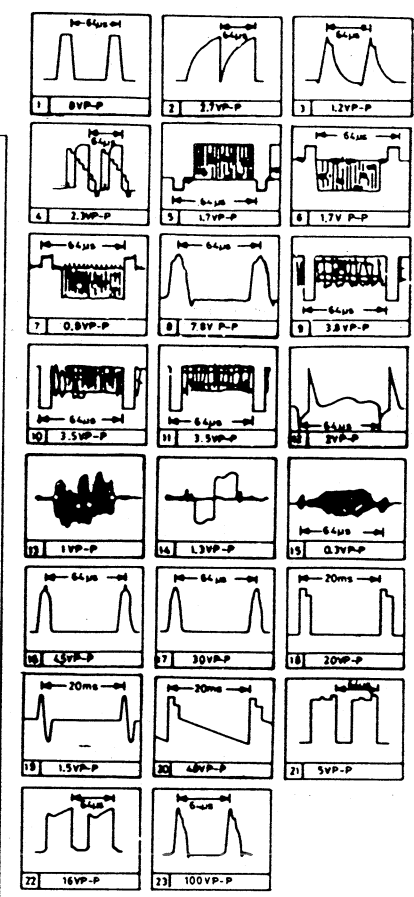
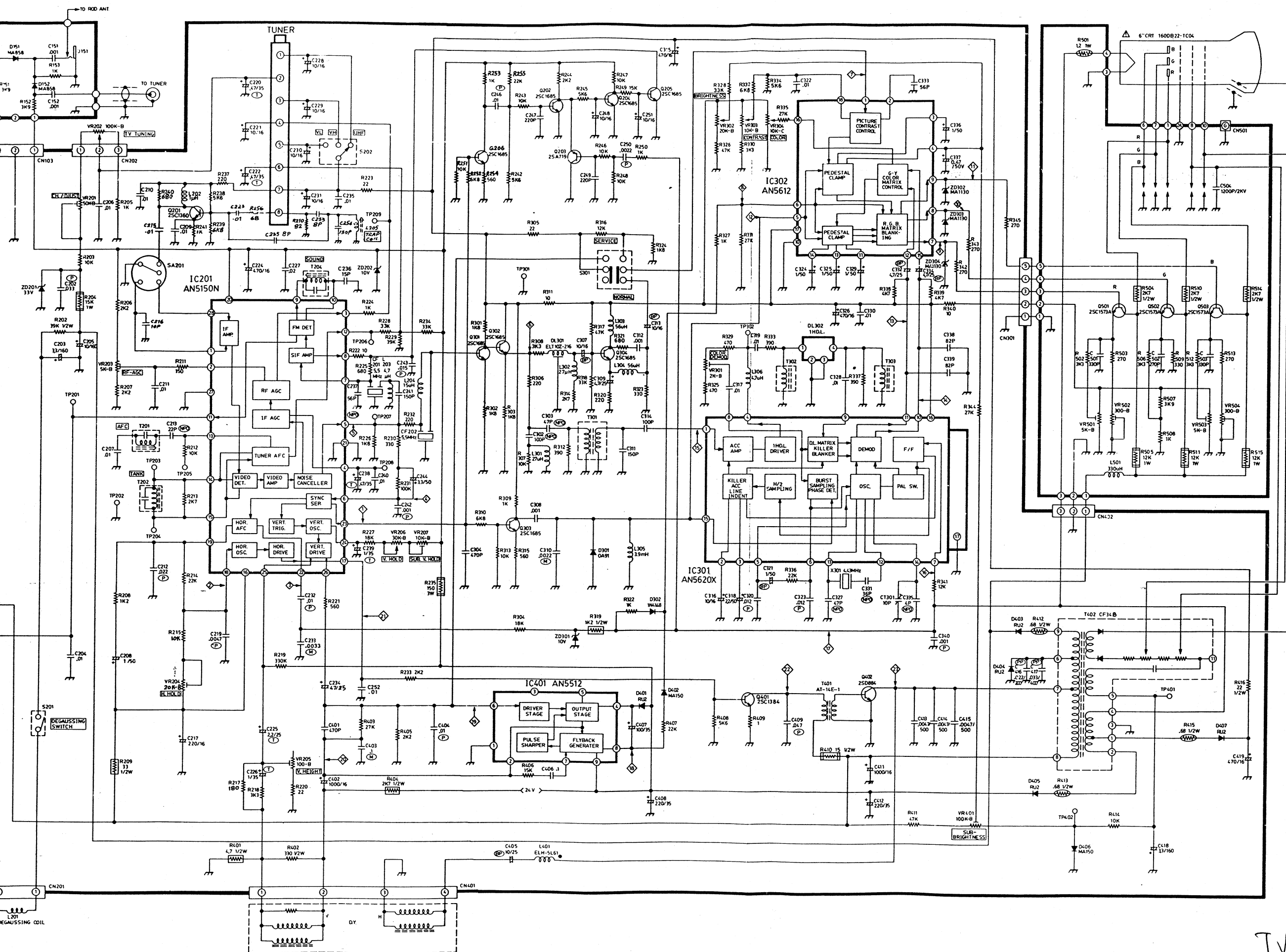


# SCHEMATIC DIAGRAM FOR PAL-B/G(FTZ) SYSTEM





- NOTE:**
1. ALL CAPACITORS ARE IN  $\mu F$  UNLESS OTHERWISE NOTED. ALL CAPACITORS ARE 50VW UNLESS OTHERWISE NOTED.
  2. CAPACITOR NOT SPECIFICALLY DESIGNATED ARE CERAMIC CAPACITORS.
    - a.  $\text{ELECTROLYTIC CAPACITOR}$
    - b.  $\text{BI-POLAR ELECTROLYTIC CAPACITOR}$
    - c.  $\text{TANTALUM CAPACITOR}$
    - d.  $\text{METALLIZED POLYESTER}$
    - e.  $\text{POLYESTER FILM CAPACITOR}$
    - f.  $\text{POLYPROPYLENE CAPACITOR}$
    - g.  $\text{MYLAR CAPACITOR}$
  3. ALL RESISTORS ARE IN OHM 1/4 WATT UNLESS OTHERWISE NOTED.
  4. RESISTOR NOT SPECIFICALLY DESIGNATED ARE CARBON FILM RESISTORS.
    - a.  $\text{NONFLAMMABLE RESISTOR}$
    - b.  $\text{FUSEBLE RESISTOR}$
    - c.  $\text{CEMENT RESISTOR}$
    - d.  $\text{METAL OXIDE RESISTOR}$
    - e.  $\text{THERMISTOR}$
  5. DC VOLTAGE ARE MEASURED FROM POINTS INDICATED TO THE CIRCUIT GROUND WITH A MULTI-METER TEST.
  6. WAVEFORMS ARE TAKEN WITH SETTING CONTROLS TO A NORMAL CONDITIONS (COLOR BAR PATTERN).
  7. THIS CIRCUIT DIAGRAM IS SUBJECT TO CHANGE WITHOUT PRIOR NOTICE.

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